

## Cisco 2-Port Channelized OC-12/STM-4 Shared Port Adapter

The Cisco® I-Flex design combines shared port adapters (SPAs) and SPA interface processors (SIPs) using an extensible design that enables service prioritization for data, voice, and video services. Enterprise and service provider customers can use the improved slot economics resulting from modular port adapters that are interchangeable across Cisco routing platforms.

The I-Flex design maximizes connectivity options and offers superior service intelligence through programmable interface processors that deliver line-rate performance. I-Flex enhances speed-to-service revenue and provides a rich set of quality-of-service (QoS) features for premium service delivery while effectively reducing the overall cost of ownership. This data sheet contains the specifications for the Cisco 2-Port Channelized OC12/STM-4 SPA (Figure 1).

**Figure 1.** Cisco 2-Port Channelized OC-12/STM-4 Shared Port Adapter



### Product Overview

The Cisco 2-Port Channelized OC-12/STM-4 SPA is available on high-end Cisco routing platforms offering the benefits of network scalability with lower initial costs and ease of upgrades. The Cisco SPA/SIP portfolio continues the Cisco focus on investment protection along with consistent feature support, broad interface availability, and the latest technology. The Cisco SPA/SIP portfolio allows different interfaces (Packet over SONET/SDH [POS], ATM, Ethernet, etc.) to be deployed on the same interface processor.

## Applications

Internet traffic continues to grow at an explosive rate, with the number of users doubling each year. This unsurpassed growth is increasing the demand for higher-capacity interfaces from OC-3/STM-1 to OC-48/STM-16. In addition, POS enables efficient transport of IP traffic and uses SONET/SDH concatenated payloads (that is, STS-3c/VC-4, STS-12c/VC-4-4c, or STS-48c/VC-4-16c) to achieve a higher order of statistical multiplexing. At present, concatenated services at VC-4 rates are available only in some parts of the world (for example, Europe), with higher rates not yet readily available. The new channelized OC-12/STM-4 SPA provides customers in these areas with an STM-4 POS interface that can be provisioned through the traditional network. The Cisco 2-Port Channelized OC-12/STM-4 SPA builds on the SONET/SDH channelization capability.

## Key Features

- Two channelized OC-12/STM-4 ports per SPA
- SONET mappings
  - STS-12c
  - STS-3c
  - STS-1<>DS3 (clear channel)
  - PDH DS3 (M13 & C-bit) STS-1<>DS3<>DS1
  - PDH DS3 (M13 & C-bit) STS-1<>DS3<>DS1<>nxDS0
  - STS-1<>VT1.5<>DS1
  - STS-1<>VT1.5<>DS1<>nxDS0
- SDH mappings (please check platform documentation for support)
  - STS-1<>DS3<>E1
  - STS-1<>VT2<>E1
  - STS-1<>VT1.5<>DS1<>nxDS0
  - STS-1<>VT2<>E1<>nxDS0
  - STS-1<>DS3<>DS1<>nxDS0
  - STS-1<>DS3<>E1<>nxDS0
  - AU-4-4c
  - AU-4
  - AU-4<>TUG-3<>VC-3<>DS3
  - AU-4<>TUG-3<>VC-3<>E3
  - AU-3<>VC-3<>DS3
  - AU-3<>VC-3<>E3
  - AU-4<>TUG-3<>TUG-2<>VC-12<>E1
  - AU-3<>TUG-2<>VC-12<>E1
  - AU-3<>TUG-2<>VC-11<>DS1
  - AU-4<>TUG-3<>TUG-2<>VC-12<>E1<>nxDS0
  - AU-3<>TUG-2<>VC-11<>DS1<>nxDS0
  - AU-3<>TUG-2<>VC-12<>E1<>nxDS0

- Channelization support for 4 OC-3 or 12 DS-3 or 336 DS-1 or 2000 DS-0 or n x DS-0 channels, or any combination of these, to a total of 2000 channels
- Multilink bundling on T1/E1 channels as per RFC 1990
- Subrate DS-3
- Bit Error Rate Test (BERT) support on T1/E1 channels  
all 0s, all 1s, 2<sup>11</sup>, 2<sup>15</sup>, 2<sup>20</sup>, 2<sup>20</sup> QRSS, 2<sup>23</sup>, alternating 0s and 1s, 1-in-8, 3-in-24, Bit error insertion, Framed all zeroes, Framed all ones
- BERT support on T3/E3 channels  
T3/E3: 2<sup>23</sup>-1 PRBS (inverted), Framed all ones, Framed all zeroes, Bit error insertion, 2<sup>15</sup>, 2<sup>20</sup>

## Product Specifications

Table 1 shows the hardware specifications for the channelized OC-12/STM-4 SPA.

**Table 1.** Hardware Specifications for the Cisco 2-Port Channelized OC-12 SPA

Features	Descriptions
<b>Product compatibility</b>	Cisco 10000 Series Routers
<b>Port density per SPA</b>	2-port channelized OC-12/STM-4
<b>Physical interface (channelized OC-12)</b>	OC-12c/STM-4 Small Form-Factor Pluggable (SFP) optics module (refer to optical parameters in Table 2) Visual status indicators (LEDs): <ul style="list-style-type: none"> <li>• SPA status LED</li> <li>• Per-port LEDs</li> <li>• Carrier and alarm</li> <li>• Active and loopback</li> </ul>
<b>Protocols</b>	IETF RFC 1490, Frame Relay Encapsulation RFC 1662, Point-to-Point Protocol (PPP) in High-Level Data Link Control (HDLC)-like framing Cisco HDLC Multilink PPP (RFC 1990) (as applicable for 2-port channelized OC-12c/STM-4 (DS-1/E1) FRF.12, link fragmentation and interleaving (LFI) PPP, HDLC, and Frame Relay User Network Interface (UNI) encapsulations
<b>Features and functions</b>	<b>DS-3</b> <ul style="list-style-type: none"> <li>• Full- and half-duplex connectivity at DS-3 rate (44.736 MHz)</li> <li>• Scrambling and subrate support of major data-service-unit (DSU) vendors</li> <li>• C-bit or M23 framing (software selectable)</li> <li>• MDL</li> <li>• Support for 16- and 32-bit cyclic redundancy check (CRC) (16-bit default)</li> <li>• 24-hour history maintained for error statistics and failure counts</li> <li>• DS-3 alarm and event detection (once-per-second polling)</li> <li>• Alarm indication signal (AIS)</li> <li>• Out of frame (OOF)</li> <li>• Far-end receive failure (FERF)</li> </ul> <b>DS-1</b> <ul style="list-style-type: none"> <li>• Superframe (SF) and Extended Superframe (ESF) support</li> <li>• ANSI T1.403 FDL support</li> <li>• Internal or line-derived (loop) clocking, independently selectable on each T1 tributary</li> <li>• Error and alarm detection: CRC errors, framing errors, loss of frame (red), AIS (blue), remote alarm indication (yellow)</li> <li>• Alarm reporting: 24-hour history maintained, 15-minute intervals on all errors</li> <li>• BERT at T1 level</li> <li>• Loopback capabilities: local, network line and network payload, remote network line, and remote network payload at the T1 level</li> </ul>

Features	Descriptions
<b>SONET errors, alarms, and performance monitoring</b>	<p>Synchronization:</p> <ul style="list-style-type: none"> <li>• Local (internal) or loop timed (recovered from network)</li> <li>• Pointer activity monitoring</li> <li>• Redundant clock source should be provisioned on different SPAs as only a single timing source can be provided by SPA</li> </ul> <p>Local (diagnostic) and line (network) loopback</p> <p>Payload mapping</p> <ul style="list-style-type: none"> <li>• POS with 1 + X<sup>43</sup> self-synchronous scrambler</li> </ul> <p>SONET/SDH compliance:</p> <ul style="list-style-type: none"> <li>• Telcordia (Bellcore) GR-253-CORE (as applicable)</li> <li>• ANSI T1.105, T1.231</li> <li>• ITU-T G.707, G.957, G.825 (as applicable)</li> </ul> <p>Supported SONET/SDH alarm and signal events:</p> <ul style="list-style-type: none"> <li>• Signal failure bit error rate (SF-ber)</li> <li>• Signal degrade bit error rate (SD-ber)</li> <li>• Signal label payload construction (C2)</li> <li>• Path trace byte (J1)</li> <li>• Section</li> <li>• Loss of signal (LoS)</li> <li>• Loss of frame (LoF)</li> <li>• Error counts for B1</li> <li>• Threshold crossing alarms (TCA) for B1</li> <li>• Line</li> <li>• Line alarm indication signal (LAIS)</li> <li>• Line remote defect indication (LRDI)</li> <li>• Line remote error indication (LREI)</li> <li>• Error counts for B2</li> <li>• TCA for B2</li> <li>• Path</li> <li>• Path alarm indication signal (PAIS)</li> <li>• Path remote defect indication (PRDI)</li> <li>• Path remote error indication (PREI)</li> <li>• Error counts for B3</li> <li>• TCA for B3</li> <li>• Loss of pointer (LoP)</li> <li>• Positive stuffing event (PSE)</li> <li>• Negative stuffing event (NSE)</li> <li>• Path unequipped indication signal (PUNEQ)</li> <li>• Path payload mismatch indication signal (PPLM)</li> </ul>
<b>Network management</b>	<p>SONET-MIB (RFC 3592)</p> <p>SONET/SDH-MIB (RFC 2558)</p> <p>CISCO-SONET-MIB</p> <p>DS-3/E3-MIB (RFC 3896)</p> <p>DS-1/E1-MIB (RFC 2496)</p> <p>Simple Network Management Protocol (SNMP)</p>
<b>Reliability and availability</b>	<p>Online insertion and removal (OIR)</p> <p>Field-replaceable SFP optical modules</p> <p>1+1 SONET Automatic Protection Switching (APS) and SDH Linear Multiplexer Section Protection (MSP) protocols (please check platform support)</p> <p>Single SPA software reset</p>
<b>Physical specifications</b>	<p>Weight: 1.27 lb (0.57 kg)</p> <p>Height: 1.56 in. (3.96 cm) (double height)</p> <p>Width: 6.75 in. (17.15 cm)</p> <p>Depth: 7.28 in. (18.49 cm)</p>
<b>Power</b>	2-port channelized OC-12/STM-4 = 52.0W (with optics)

Features	Descriptions
<b>Environmental specifications</b>	Operating temperature: 41 to 104°F (5 to 40°C) Storage temperature: -38 to 150°F (-40 to 70°C) Operating humidity: 5 to 85% relative humidity Storage humidity: 5 to 95% relative humidity
<b>Compliance and agency approvals</b>	<p><b>Safety</b></p> <ul style="list-style-type: none"> <li>• UL 60950</li> <li>• CSA 22.2-No.60950</li> <li>• EN60950</li> <li>• IEC 60950 CB Scheme</li> <li>• ACA TS001</li> <li>• AS/NZS 3260</li> <li>• EN60825\IEC60825 laser safety (SR, IR-Class 1) (VSR-Class 1M)1</li> <li>• 21CFR1040 -FDA Code of Federal Regulations (USA) laser safety (SR, IR-Class 1) (VSR-Class 1M)1</li> </ul> <p><b>EMC</b></p> <ul style="list-style-type: none"> <li>• FCC Part 15 (CFR 47)</li> <li>• ICES 003</li> <li>• EN55022</li> <li>• CISPR 22</li> <li>• AS/NZS CISPR22</li> <li>• VCCI</li> <li>• EN55024</li> <li>• EN50082-1</li> <li>• EN61000-6-1</li> <li>• EN61000-3-2</li> <li>• EN61000-3-3</li> </ul> <p><b>Network Equipment Building Standards (NEBS)</b></p> <p>This product is designed to meet the following requirements (official qualification may be in progress):</p> <ul style="list-style-type: none"> <li>• SR-3580 – NEBS: Criteria levels (Level 3 compliant)</li> <li>• GR-63-CORE – NEBS: Physical protection</li> <li>• GR-1089-CORE – NEBS: EMC and safety</li> </ul>

**Table 2.** Optical Specifications for the Cisco 2-Port Channelized OC-12 SPA

SFP Optics	Maximum Distance
Multimode (MM)	Up to 0.25 mi (500m)
Single-mode (SM)	Up to 1.2 mi (2 km)
SM intermediate reach (IR-1)	Up to 9 mi (15 km)
SM long reach (LR-1)	Up to 25 mi (40 km)
SM extended reach (LR-2)	Up to 50 mi (80 km)

## Power and Environmental Requirements

When installed in Cisco routers, these SPAs do not change the power or environmental requirements and standards of the router platform itself. Refer to the platform-specific data sheets for more information.

## Ordering Information

To place an order, visit the [Cisco Ordering Home Page](#) and refer to Table 3.

**Table 3.** Ordering Information

Product Name	Part Number
Cisco 2-Port Channelized OC-12/STM-4 SPA	SPA-2XCHOC12/DS0
OC-12/STM-4, OC-12/STM-4 SFP, MM	SFP-OC12-MM
OC-12/STM-4, OC-12/STM-4 SFP, SM, SR	SFP-OC12-SR
OC-12/STM-4, OC-12/STM-4 SFP, SM, IR-1	SFP-OC12-IR1
OC-12/STM-4, OC-12/STM-4 SFP, SM, LR-1	SFP-OC12-LR1
OC-12/STM-4, OC-12/STM-4 SFP, SM, LR-2	SFP-OC12-LR2

## Service and Support

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## For More Information

For more information about the Cisco SPA/SIP portfolio, visit [www.cisco.com/go/spa](http://www.cisco.com/go/spa) or contact your local Cisco account representative.



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